

Applicant: Yoshinori Hino et al.

Art Unit

2815

Serial No.: 10/080,831

Examiner:

J. Nguyen

Filed

: February 21, 2002

Confirmation No.:

9597

Title

Notice of Allowance Date: September 24, 2004 : SEMICONDUCTOR DEVICE AND PATTERN LAYOUT METHOD THEREOF

MAIL STOP ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed September 24, 2004, enclosed are a completed issue fee transmittal form PTOL-85b and a check for \$1730 for the required issue fee and publication fee, including patent copies.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

12/21/04 Date:

Samuel Borodach Reg. No. 38,388

Fish & Richardson P.C. Citigroup Center 52nd Floor 153 East 53rd Street New York, New York 10022-4611

Telephone: (212) 765-5070 Facsimile: (212) 258-2291

30213068.doc

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

Land Forodock

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

| December 21, 2004 | |
|---|---|
| Date of Deposit | |
| Signature PAULA T. ROMEO | _ |
| Typed or Printed Name of Person Signing Certificate | _ |